

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,952,127 B2
DATED : October 4, 2005
INVENTOR(S) : Seong-hoon Lee

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1,

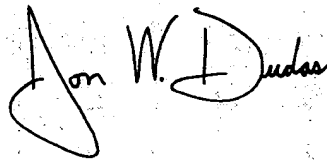
Line 63, delete "lo" before "conjunction".

Column 4,

Line 67, after "signal 306 as" insert -- NMOS gate 314 in block 310. The gate of NMOS transistor 332 is coupled to receive complement input signal 302' while the drain is coupled to the drain of PMOS transistor 352. --.

Signed and Sealed this

Fourteenth Day of February, 2006

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS

Director of the United States Patent and Trademark Office